

## Refine Search

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### Search Results -

Term	Documents
64-BITS	1103
64-BIT	9722
(6 AND 64-BITS).PGPB,USPT,EPAB,JPAB,DWPI,TDBD.	14
(L6 AND 64-BITS ).PGPB,USPT,EPAB,JPAB,DWPI,TDBD.	14

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**Database:** US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
**US OCR Full-Text Database**  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

**Search:** L7

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### Search History

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**DATE:** Sunday, March 21, 2004 [Printable Copy](#) [Create Case](#)

**Set Name Query**

side by side

*DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
L7	L6 and 64-bits	14	<u>L7</u>
<u>L6</u>	l3 and index\$5 adj9 attribut\$5	121	<u>L6</u>
<u>L5</u>	hierach\$6 adj9 attribute	2	<u>L5</u>
<u>L4</u>	L3 and hierachical adj9 attribute	0	<u>L4</u>
<u>L3</u>	L2 and bit adj2 map\$6	17087	<u>L3</u>
<u>L2</u>	block or space or (free adj9 space) or (free adj9 block)	3927043	<u>L2</u>
<u>L1</u>	6625614.pn.	2	<u>L1</u>

END OF SEARCH HISTORY

## Hit List

Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs
Generate OACS				

Search Results - Record(s) 1 through 14 of 14 returned.

1. Document ID: US 20030065459 A1

**Using default format because multiple data bases are involved.**

L7: Entry 1 of 14

File: PGPB

Apr 3, 2003

PGPUB-DOCUMENT-NUMBER: 20030065459

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030065459 A1

TITLE: Expandable intelligent electronic device

PUBLICATION-DATE: April 3, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Huber, Benedikt T.	Victoria		CA	
Ozcerin, Kurtulus H.	Sidney		CA	
Etheridge, Hal E.	Victoria		CA	
Hyatt, Geoffrey T.	Victoria		CA	
Lightbody, Simon H.	Victoria		CA	
Przydatek, Piotr B.	Victoria		CA	

US-CL-CURRENT: 702/62

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMPC	Drawn De
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2. Document ID: US 20020169920 A1

L7: Entry 2 of 14

File: PGPB

Nov 14, 2002

PGPUB-DOCUMENT-NUMBER: 20020169920

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020169920 A1

TITLE: Information processing system and semiconductor intergrated circuit

PUBLICATION-DATE: November 14, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kanno, Yusuke	Hachioji		JP	

Mizuno, Hiroyuki	Kokubunji	JP
Watanabe, Takao	Fucyu	JP

US-CL-CURRENT: 711/3; 711/156, 711/202, 711/5

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn D](#)

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3. Document ID: US 20020145611 A1

L7: Entry 3 of 14

File: PGPB

Oct 10, 2002

PGPUB-DOCUMENT-NUMBER: 20020145611  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020145611 A1

TITLE: Video controller system with object display lists

PUBLICATION-DATE: October 10, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Dye, Thomas A.	Austin	TX	US	
Geiger, Peter D.	Austin	TX	US	
Alvarez, Manuel J. II	Austin	TX	US	

US-CL-CURRENT: 345/543; 345/536, 345/555

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn D](#)

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4. Document ID: US 20020135585 A1

L7: Entry 4 of 14

File: PGPB

Sep 26, 2002

PGPUB-DOCUMENT-NUMBER: 20020135585  
PGPUB-FILING-TYPE: new  
DOCUMENT-IDENTIFIER: US 20020135585 A1

TITLE: Video controller system with screen caching

PUBLICATION-DATE: September 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Dye, Thomas A.	Austin	TX	US	
Geiger, Peter D.	Austin	TX	US	
Alvarez, Manuel J. II	Austin	TX	US	

US-CL-CURRENT: 345/531

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIWC	Drawn De
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5. Document ID: US 6567091 B2

L7: Entry 5 of 14

File: USPT

May 20, 2003

US-PAT-NO: 6567091

DOCUMENT-IDENTIFIER: US 6567091 B2

TITLE: Video controller system with object display lists

DATE-ISSUED: May 20, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dye; Thomas A.	Austin	TX		
Geiger; Peter D.	Austin	TX		
Alvarez, II; Manuel J.	Austin	TX		

US-CL-CURRENT: 345/501

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIWC	Drawn De
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6. Document ID: US 6438641 B1

L7: Entry 6 of 14

File: USPT

Aug 20, 2002

US-PAT-NO: 6438641

DOCUMENT-IDENTIFIER: US 6438641 B1

TITLE: Information processing apparatus using index and tag addresses for cache access

DATE-ISSUED: August 20, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kanno; Yusuke	Hachioji			JP
Mizuno; Hiroyuki	Kokubunji			JP
Watanabe; Takao	Fucyu			JP

US-CL-CURRENT: 711/5; 711/202

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIWC	Drawn De
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7. Document ID: US 6108014 A

L7: Entry 7 of 14

File: USPT

Aug 22, 2000

US-PAT-NO: 6108014  
DOCUMENT-IDENTIFIER: US 6108014 A

TITLE: System and method for simultaneously displaying a plurality of video data objects having a different bit per pixel formats

DATE-ISSUED: August 22, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dye; Thomas A.	Austin	TX		

US-CL-CURRENT: 345/544; 345/501, 345/553, 345/565

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequenced](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn D](#)

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8. Document ID: US 6067098 A

L7: Entry 8 of 14

File: USPT

May 23, 2000

US-PAT-NO: 6067098

DOCUMENT-IDENTIFIER: US 6067098 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Video/graphics controller which performs pointer-based display list video refresh operation

DATE-ISSUED: May 23, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dye; Thomas A.	Austin	TX		

US-CL-CURRENT: 345/531; 345/503, 345/553, 345/565

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequenced](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn D](#)

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9. Document ID: US 6035311 A

L7: Entry 9 of 14

File: USPT

Mar 7, 2000

US-PAT-NO: 6035311

DOCUMENT-IDENTIFIER: US 6035311 A

TITLE: Method and system for performing a boolean operation on bit strings using a maximal bit slice

DATE-ISSUED: March 7, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
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h e b b g e e e f e h ef b e

McCool; Michael W.	Pasadena	CA
Marquis; Jean A.	Pasadena	CA

US-CL-CURRENT: 708/231

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequence	Attachments	Claims	KMPC	Draum Ds
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10. Document ID: US 6006312 A

L7: Entry 10 of 14

File: USPT

Dec 21, 1999

US-PAT-NO: 6006312

DOCUMENT-IDENTIFIER: US 6006312 A

TITLE: Cachability attributes of virtual addresses for optimizing performance of virtually and physically indexed caches in maintaining multiply aliased physical addresses

DATE-ISSUED: December 21, 1999

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kohn; Leslie	Fremont	CA		
Okin; Ken	Saratoga	CA		
Greenley; Dale	Los Gatos	CA		

US-CL-CURRENT: 711/210; 711/122, 711/145, 711/146, 711/156, 711/202, 711/206,  
711/207, 711/3

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequence	Attachments	Claims	KMPC	Draum Ds
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11. Document ID: US 6002411 A

L7: Entry 11 of 14

File: USPT

Dec 14, 1999

US-PAT-NO: 6002411

DOCUMENT-IDENTIFIER: US 6002411 A

\*\* See image for Certificate of Correction \*\*

TITLE: Integrated video and memory controller with data processing and graphical processing capabilities

DATE-ISSUED: December 14, 1999

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dye; Thomas Anthony	Austin	TX		

US-CL-CURRENT: 345/542; 345/532, 345/555

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn De](#)

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12. Document ID: US 5995120 A

L7: Entry 12 of 14

File: USPT

Nov 30, 1999

US-PAT-NO: 5995120

DOCUMENT-IDENTIFIER: US 5995120 A

TITLE: Graphics system including a virtual frame buffer which stores video/pixel data in a plurality of memory areas

DATE-ISSUED: November 30, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dye; Thomas A.	Austin	TX		

US-CL-CURRENT: 345/543; 345/501, 345/542

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn De](#)

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13. Document ID: US 5974409 A

L7: Entry 13 of 14

File: USPT

Oct 26, 1999

US-PAT-NO: 5974409

DOCUMENT-IDENTIFIER: US 5974409 A

TITLE: System and method for locating information in an on-line network

DATE-ISSUED: October 26, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sanu; Sankrant	Redmond	WA		
Pearson; Alan S.	Redmond	WA		

US-CL-CURRENT: 707/3; 707/1, 707/10

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn De](#)

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14. Document ID: US 5838334 A

L7: Entry 14 of 14

File: USPT

Nov 17, 1998

US-PAT-NO: 5838334

DOCUMENT-IDENTIFIER: US 5838334 A

TITLE: Memory and graphics controller which performs pointer-based display list video refresh operations

DATE-ISSUED: November 17, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dye; Thomas A.	Austin	TX	78731	

US-CL-CURRENT: 345/503; 345/501, 345/531, 345/553, 345/781

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Search](#) | [Attachments](#) | [Claims](#) | [KMMC](#) | [Drawn D](#)

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Term	Documents
64-BITS	1103
64-BIT	9722
(6 AND 64-BITS).PGPB,USPT,EPAB,JPAB,DWPI,TDBD.	14
(L6 AND 64-BITS).PGPB,USPT,EPAB,JPAB,DWPI,TDBD.	14

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